CALL FOR PAPERS
SPECIAL SESSION ON
"Design and Control Automation"
for CODIT'17
April 5-7, 2017 – Barcelona, Spain

Session Co-Chairs:

Dr. Wael Dghais, University of Sousse, Tunisia, waeldghais@ua.pt

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Session description

This special session deals with the problem of real-time control, modelling, and processing of front-ends wired electrical and optical or wireless RF and microwave communication I/O links. The development of such system must follow an adaptive, reflexive, intelligent, and fast reconfigurable modelling design.

The goal of this session is to bring together developers and researchers from industry and academia in order to meet and discuss solutions, scientific results, and methods in solving intriguing problems in design and control automation along with reduced order and real time modelling algorithms for signal and power integrity, modelling algorithms.

The topics of interest include, but are not limited to:

- Advanced signaling/modulation techniques and transmitter's architectures
- High-speed and ultra-low-power communication I/O links design and modeling
- Reduced order modelling algorithms for signal and power integrity
- RF/microwave and mixed-signal power amplifiers
- I/O buffers behavioral modelling

SUBMISSION

Please submit your full paper choosing the right Special Session (**11-Special Session: Design and Control Automation**) on the EasyChair for CoDIT'17 website:

https://easychair.org/conferences/?conf=codit17

All papers must be written in English and should describe original work. The length of the paper is limited to a maximum of 6 pages (in the standard IEEE conference double column format).

IIMPORTANT DATES

December 4, 2016: deadline for paper submission February 2, 2017: notification of acceptance/reject

February 26, 2017: deadline for final paper and registration.